

FIG. 1

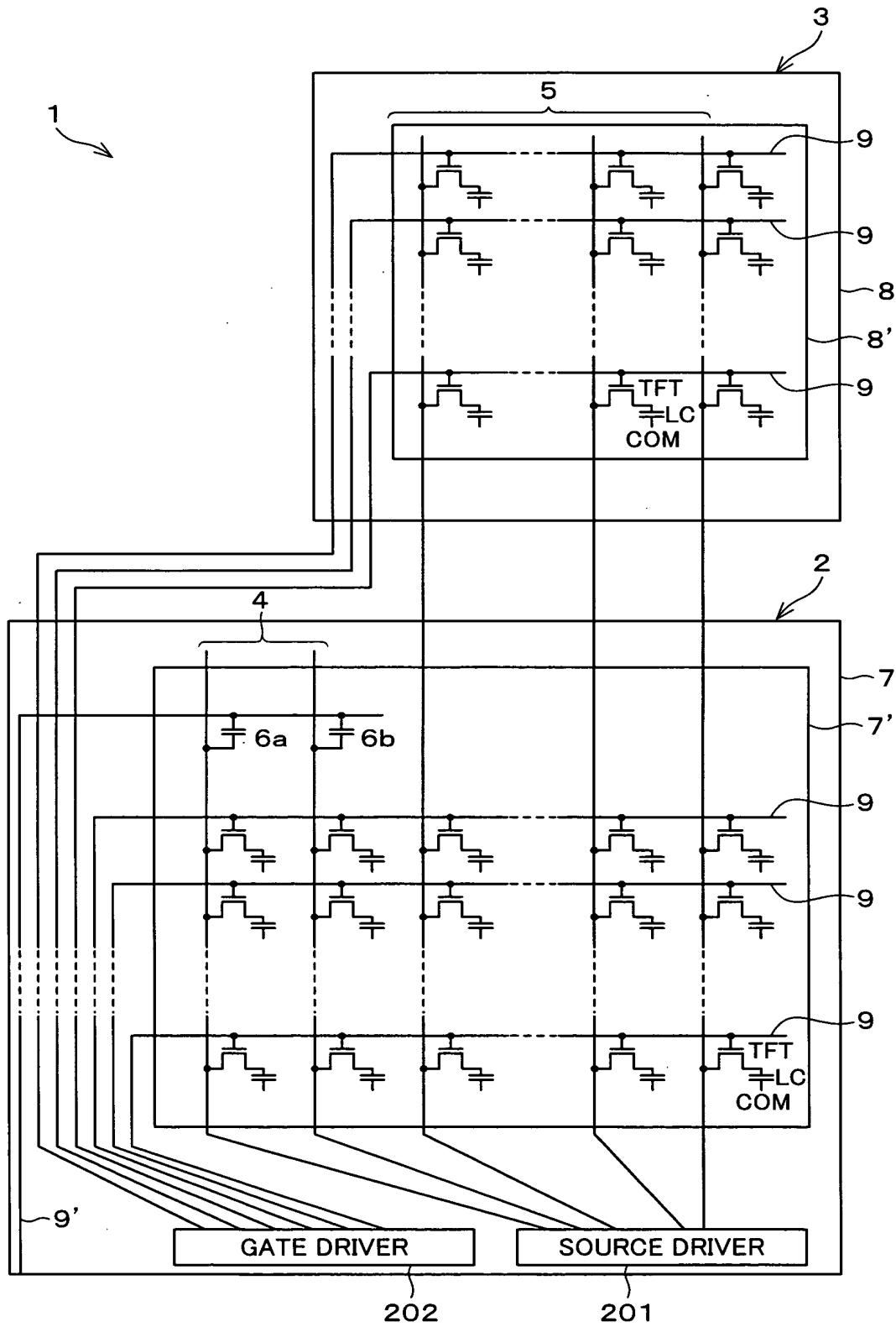


FIG. 2

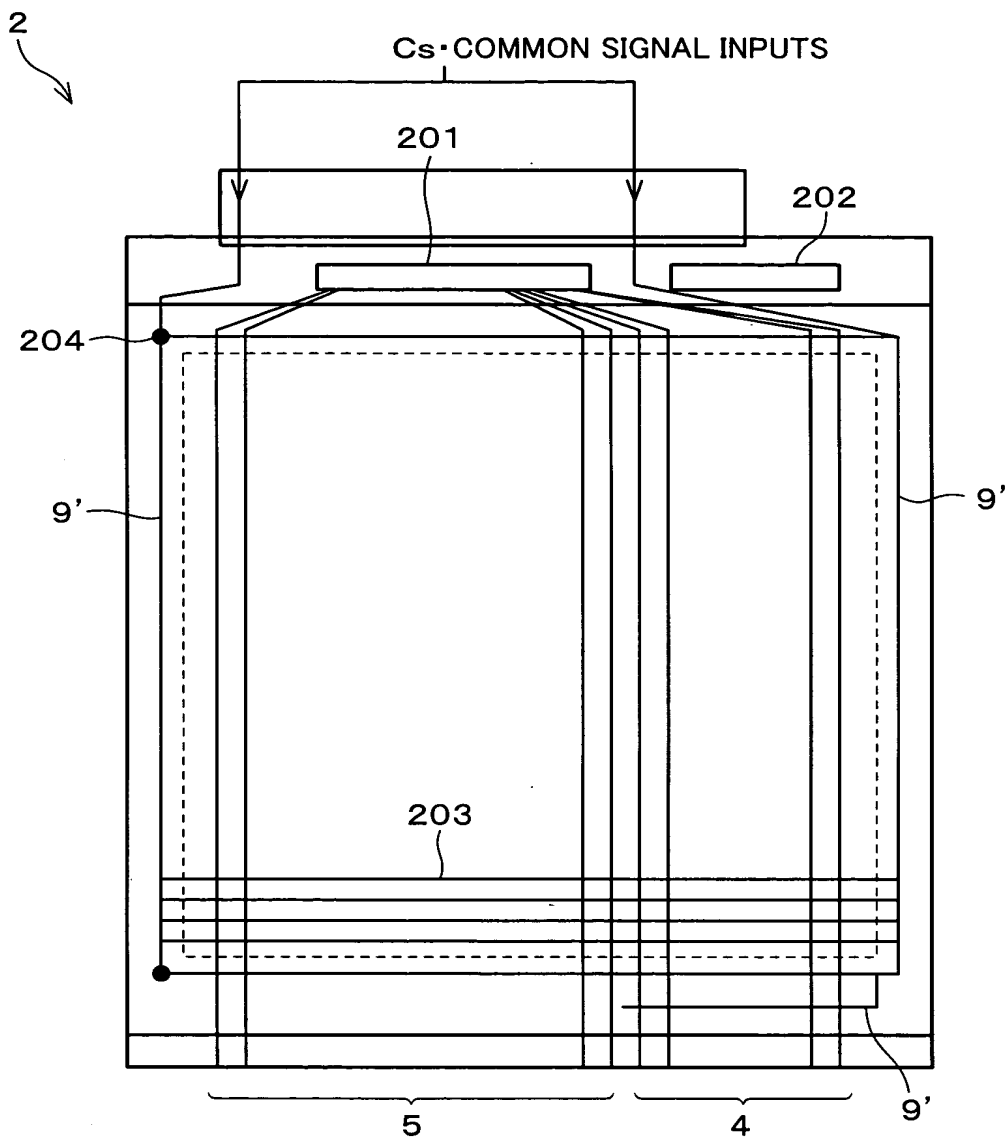
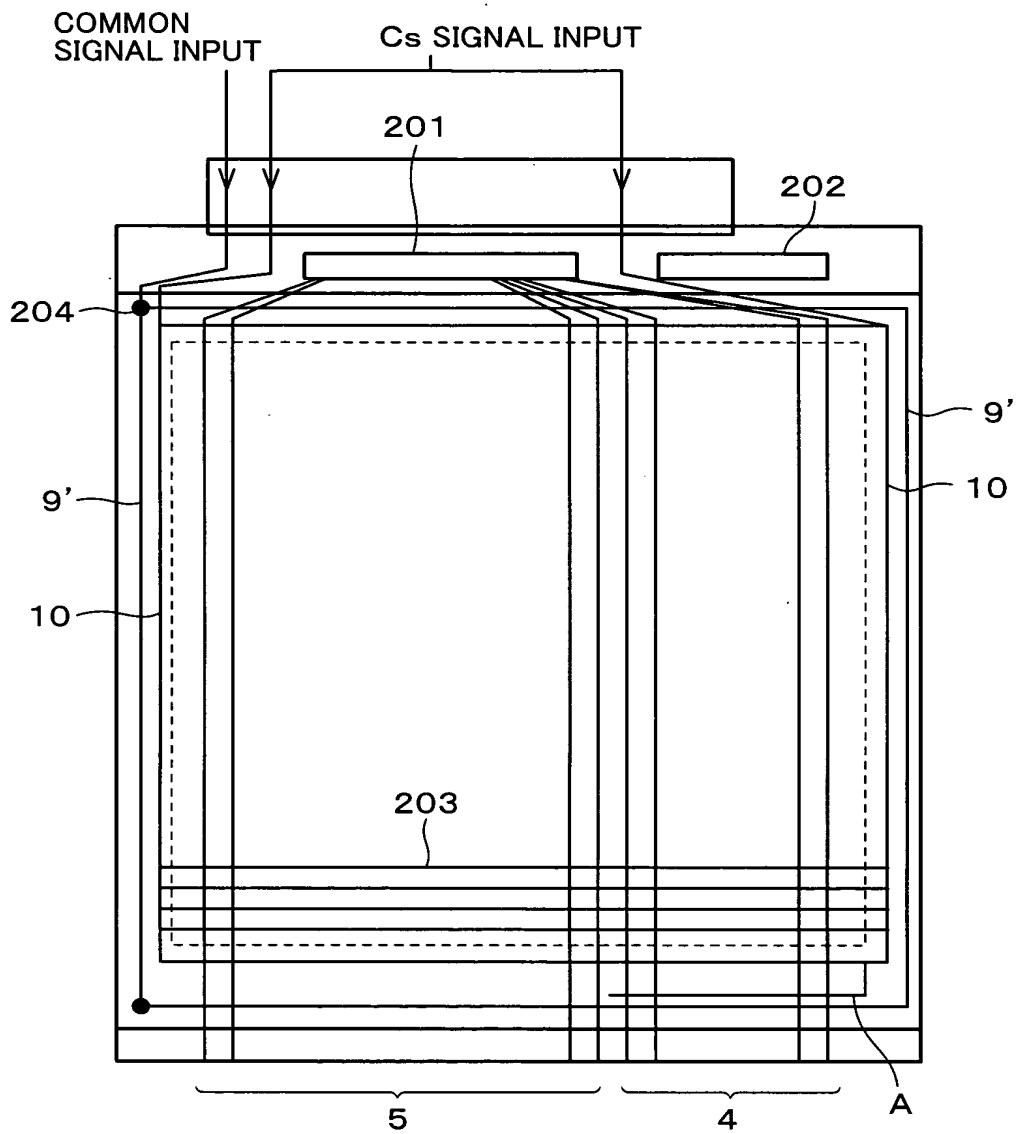


FIG. 3



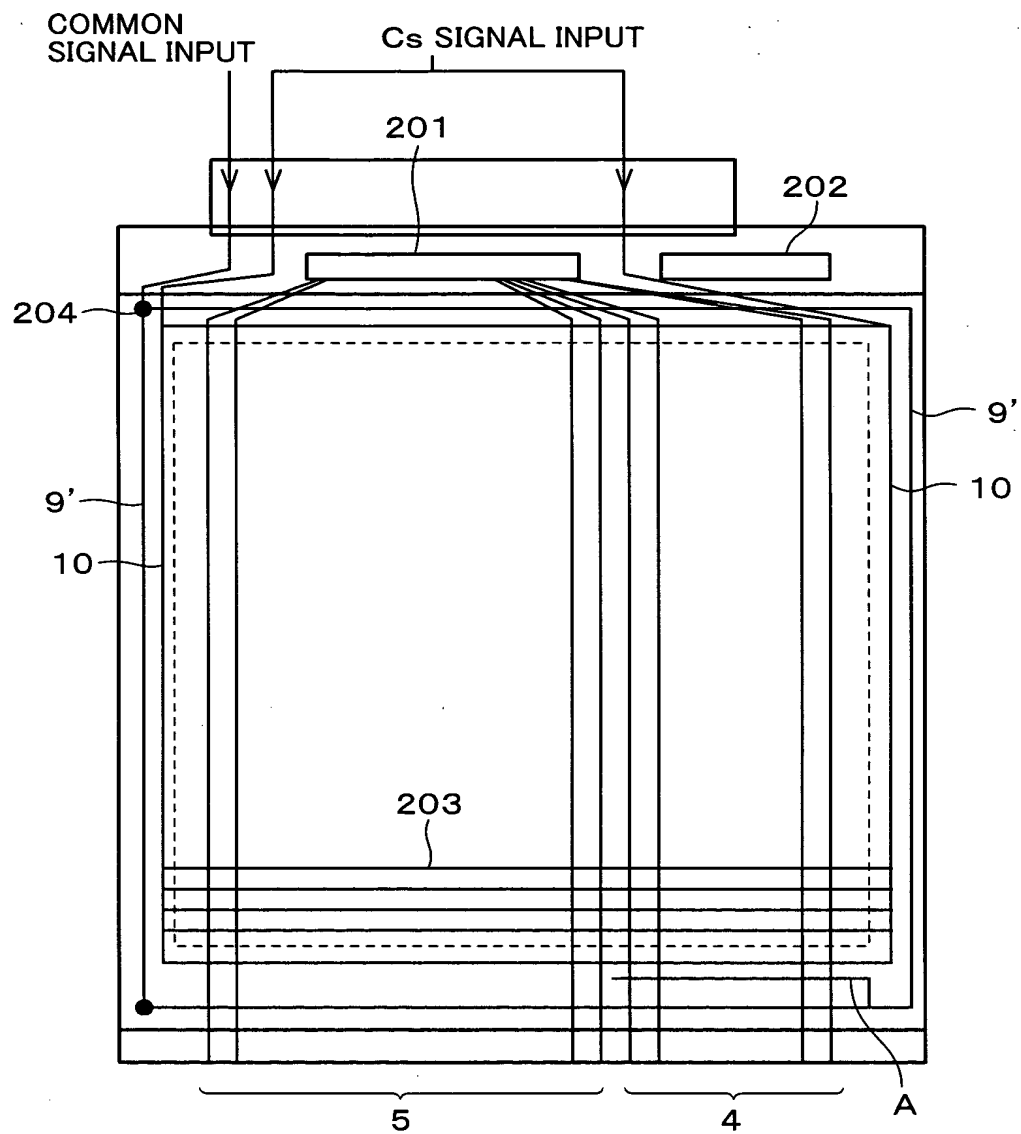


FIG. 5

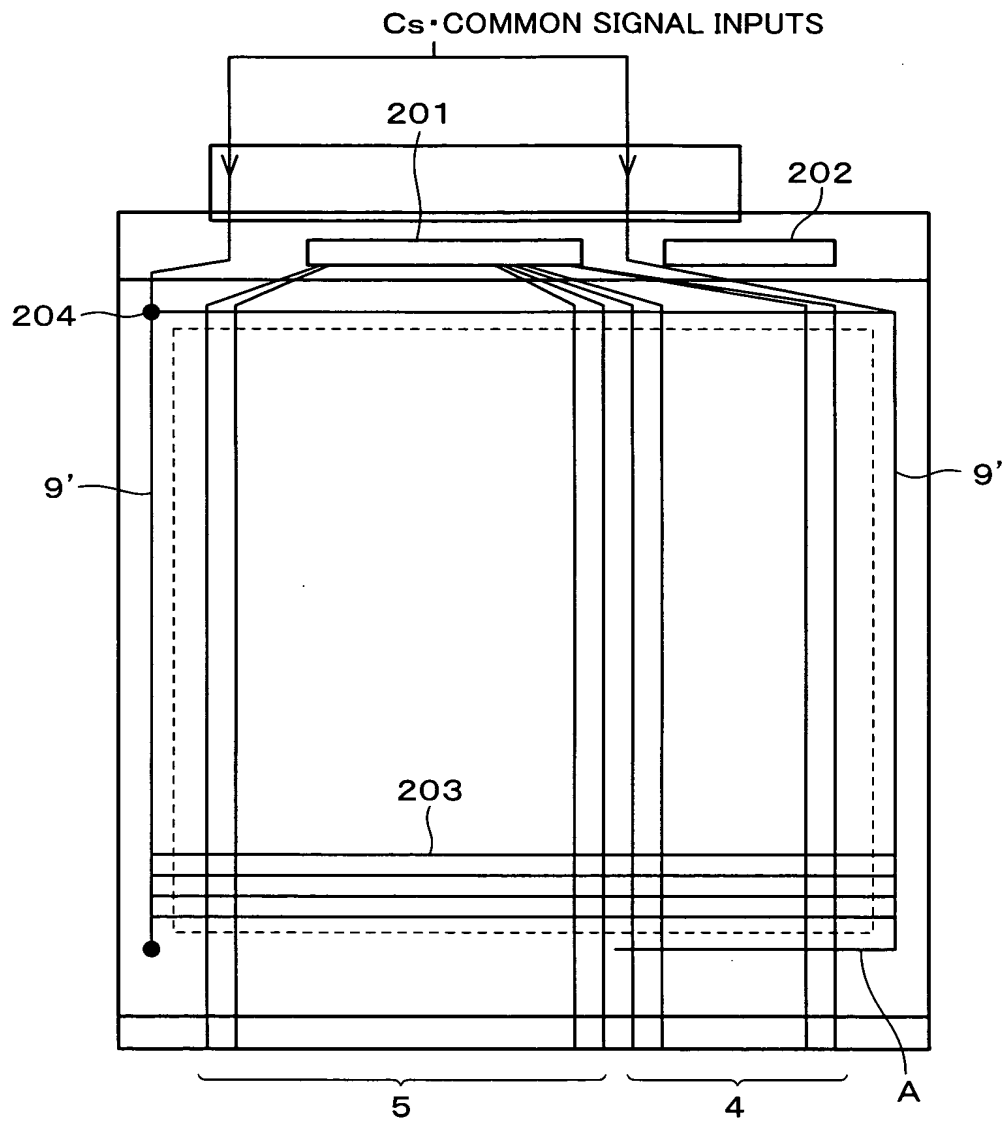


FIG. 6

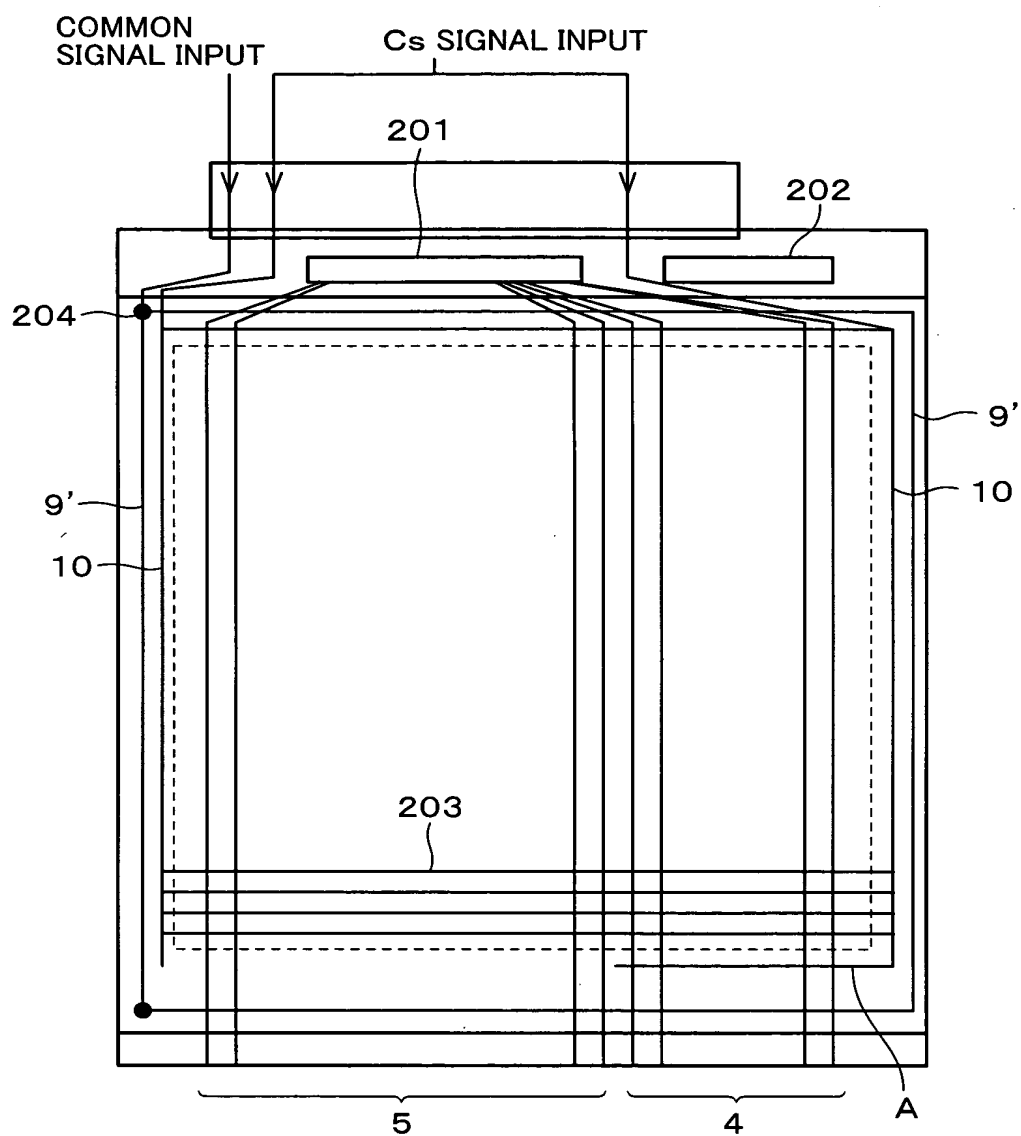


FIG. 7

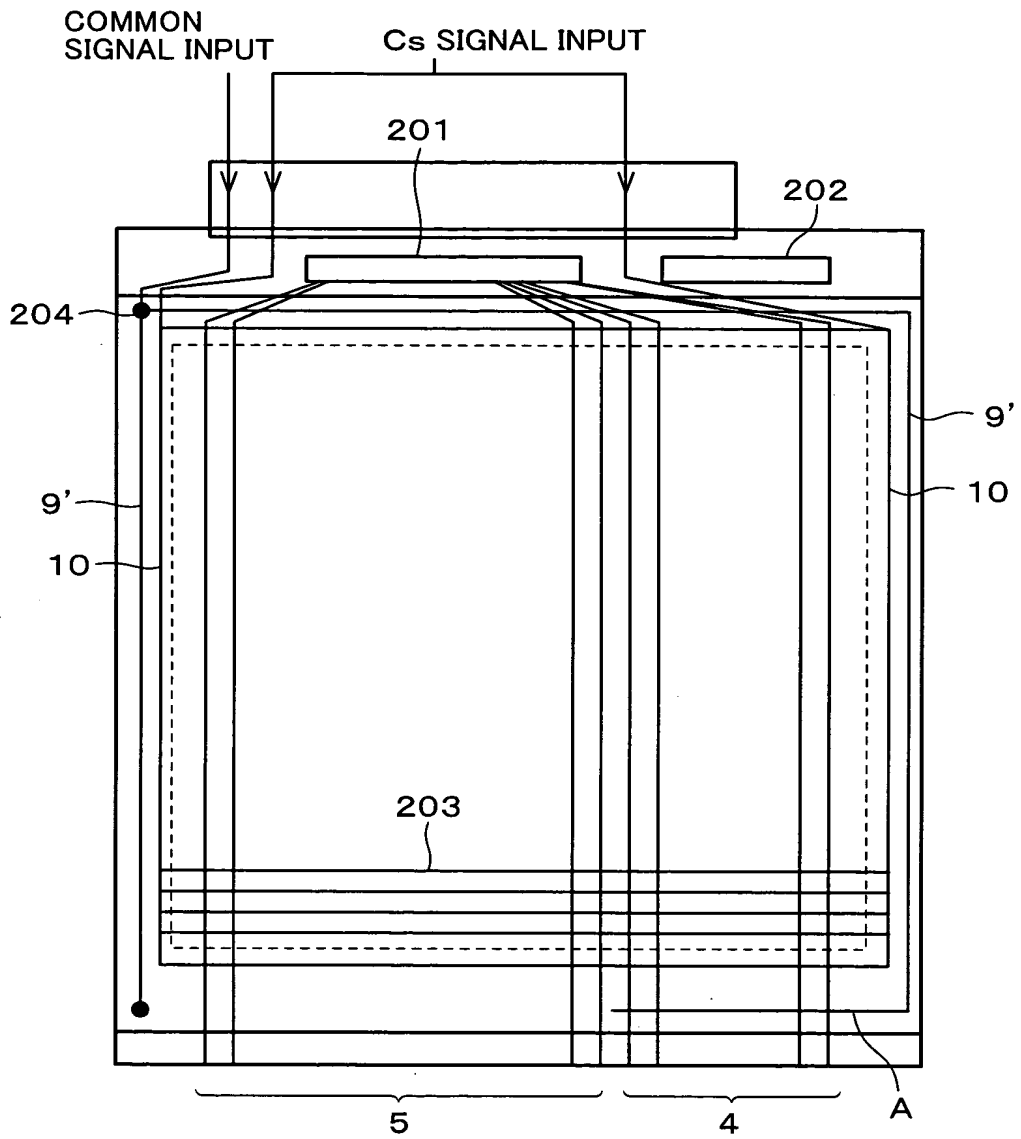


FIG. 8

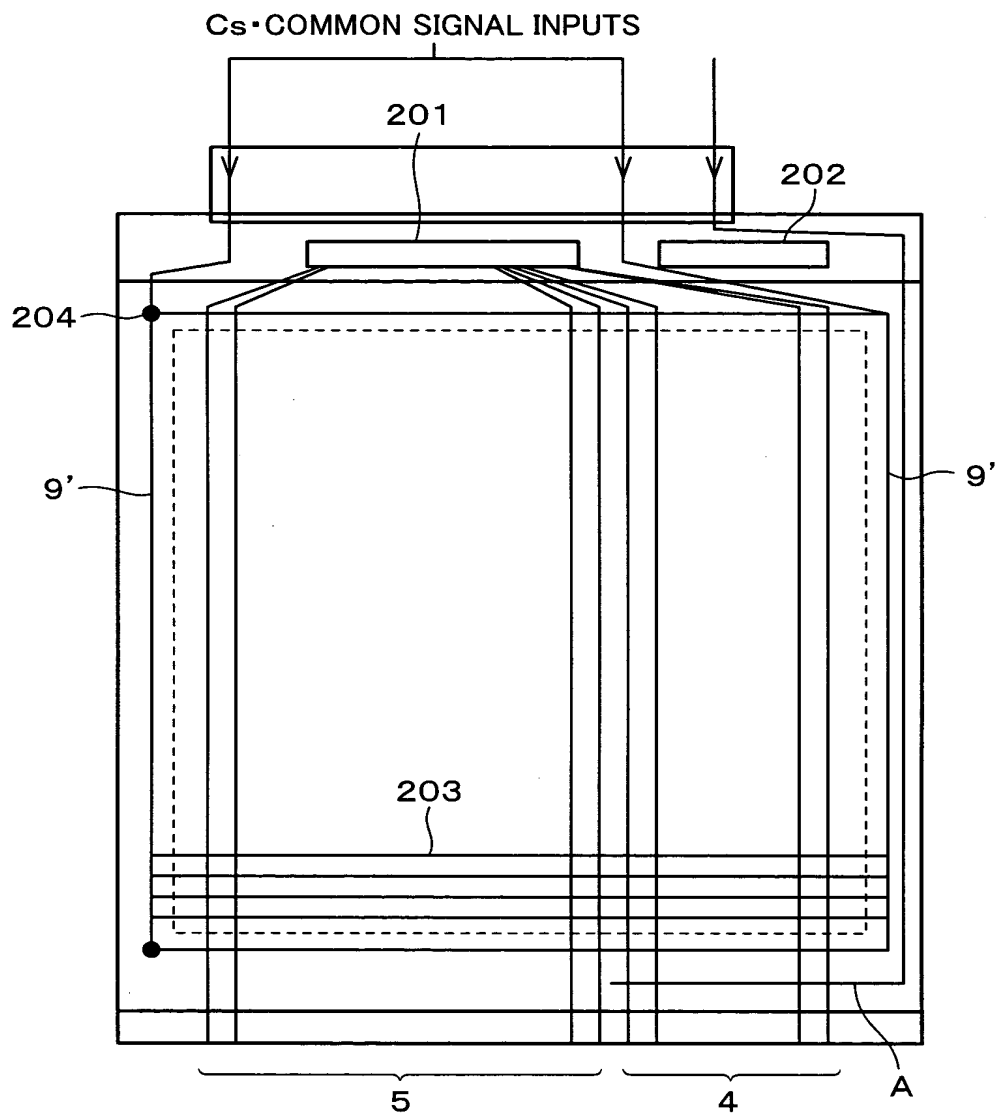




FIG. 9

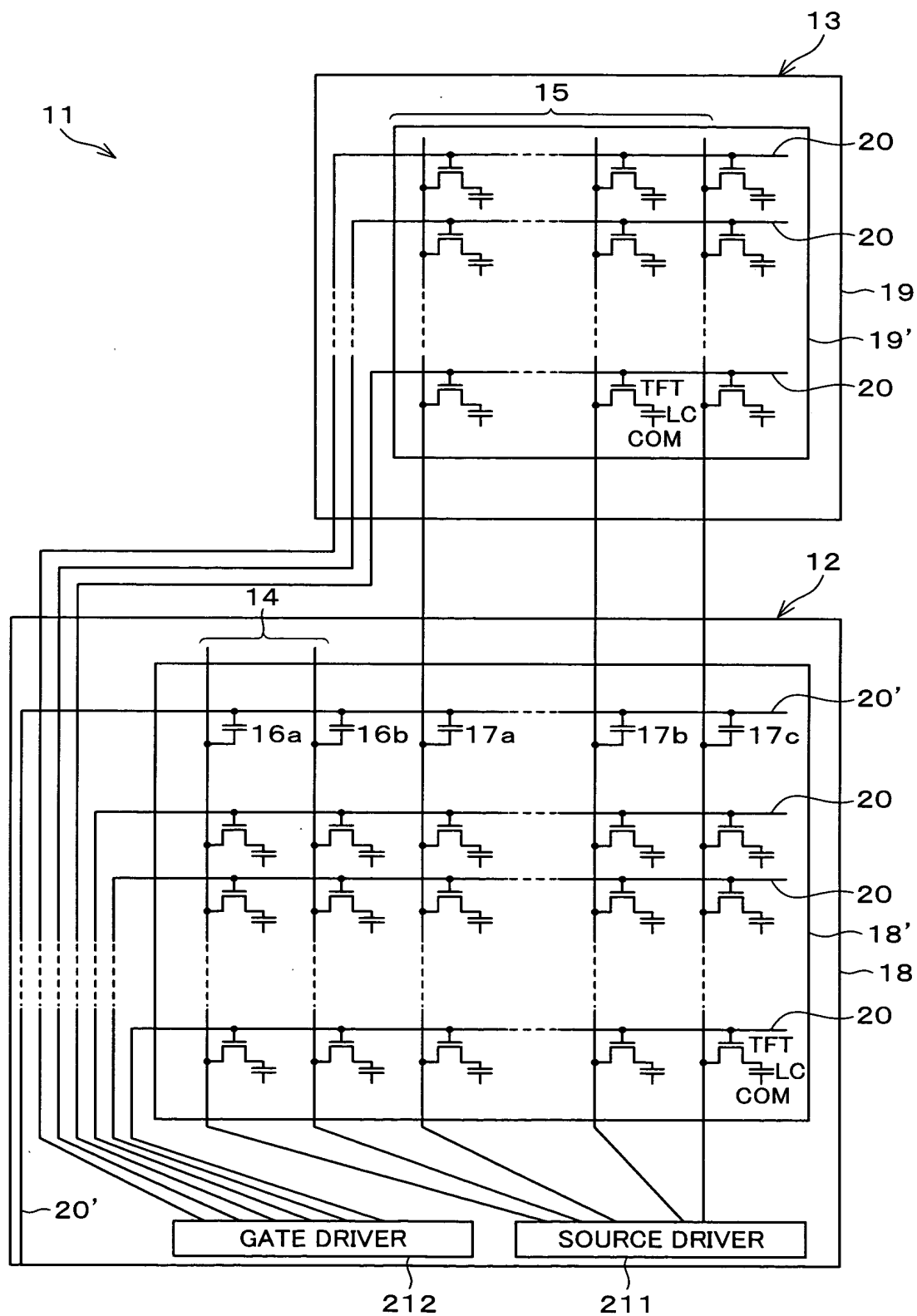


FIG. 10

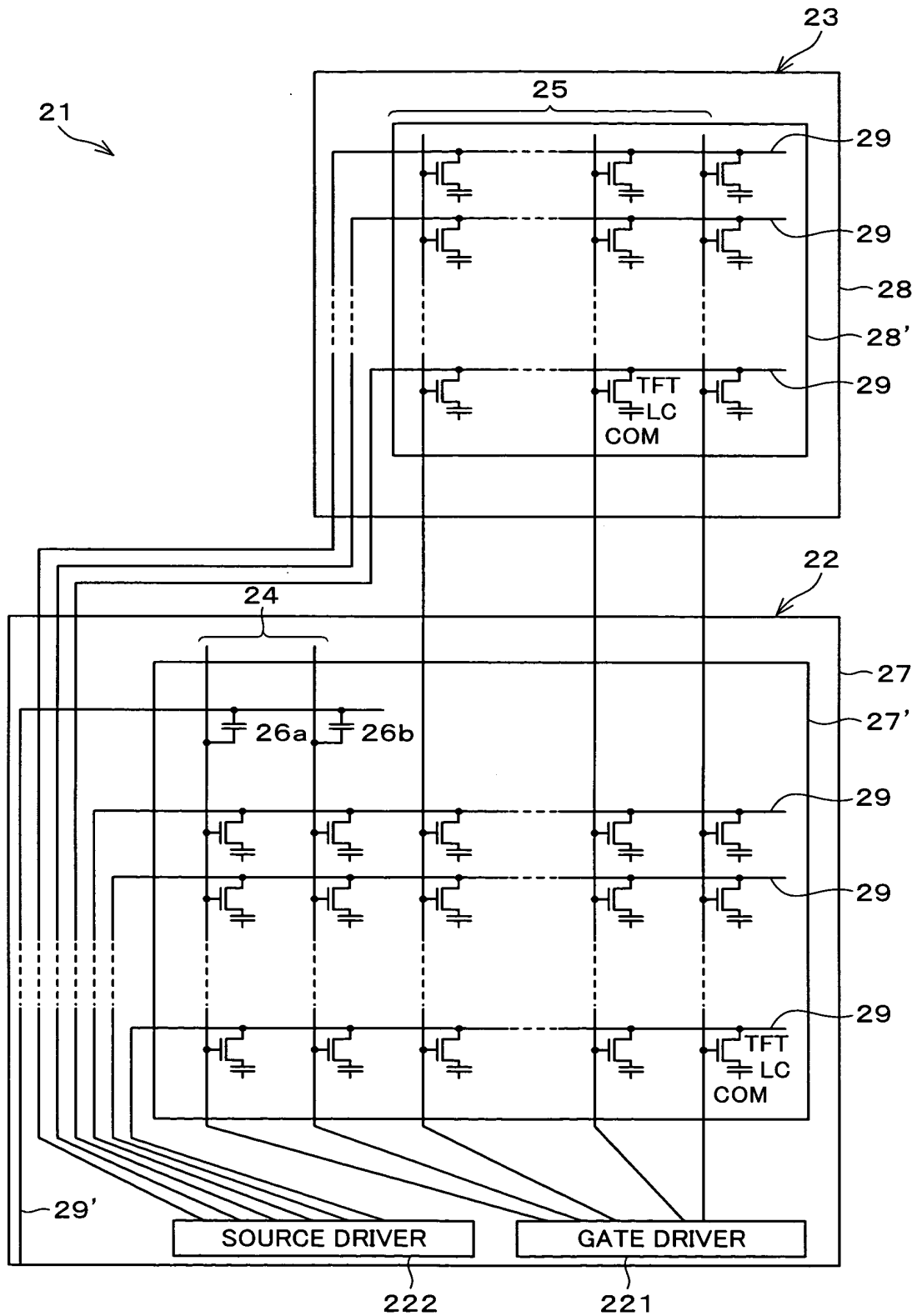


FIG. 11

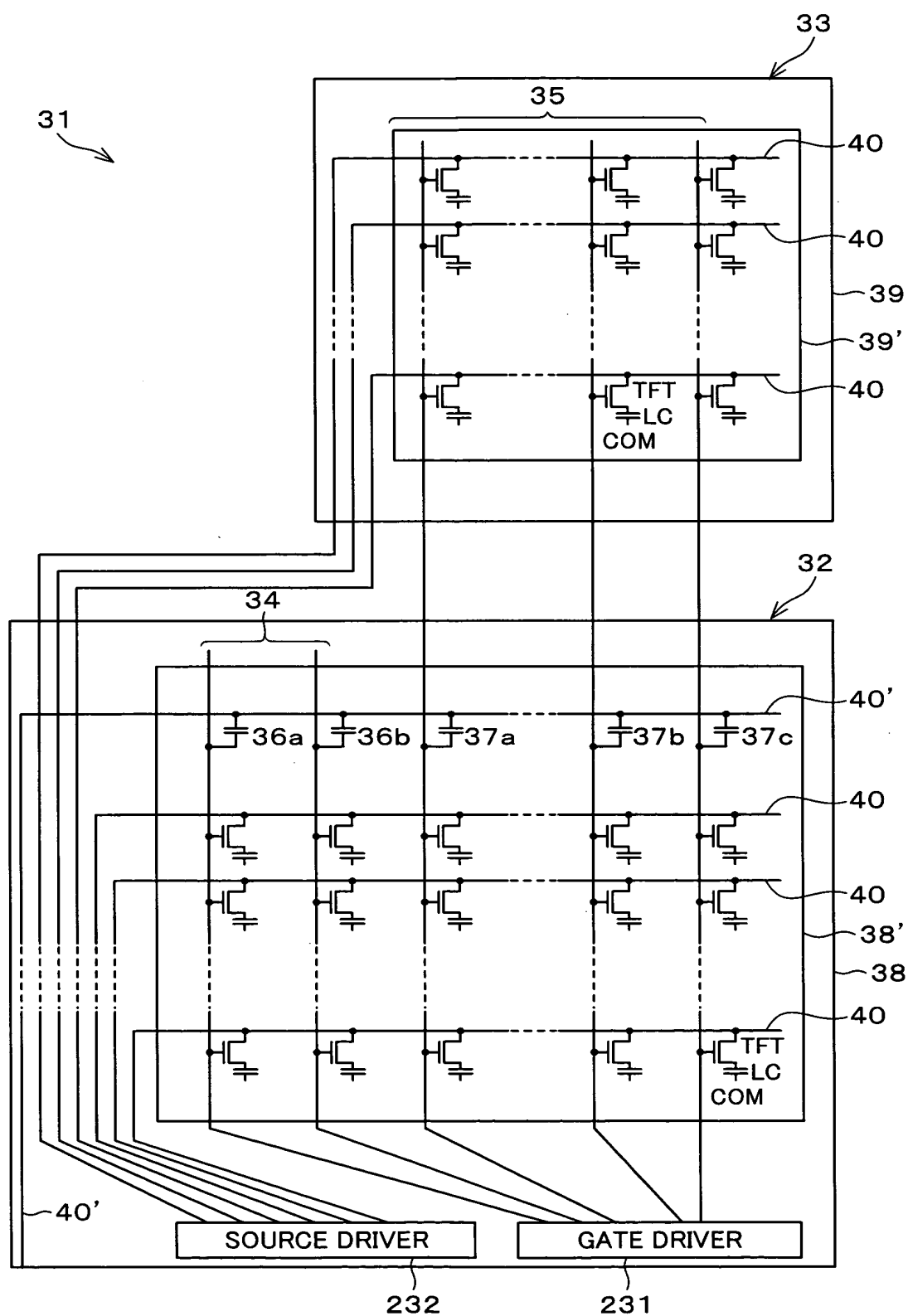


FIG. 12



FIG. 13

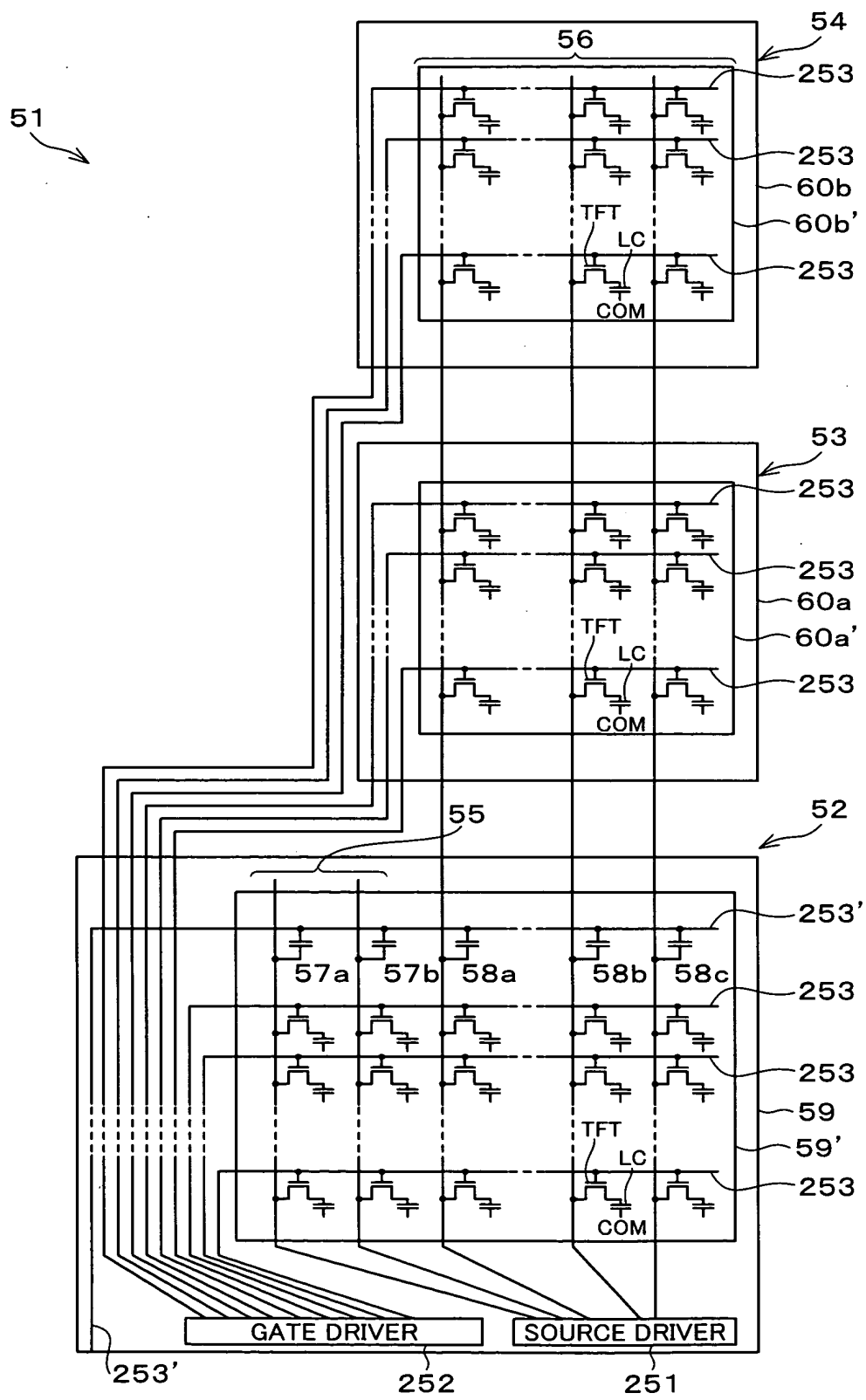


FIG. 14

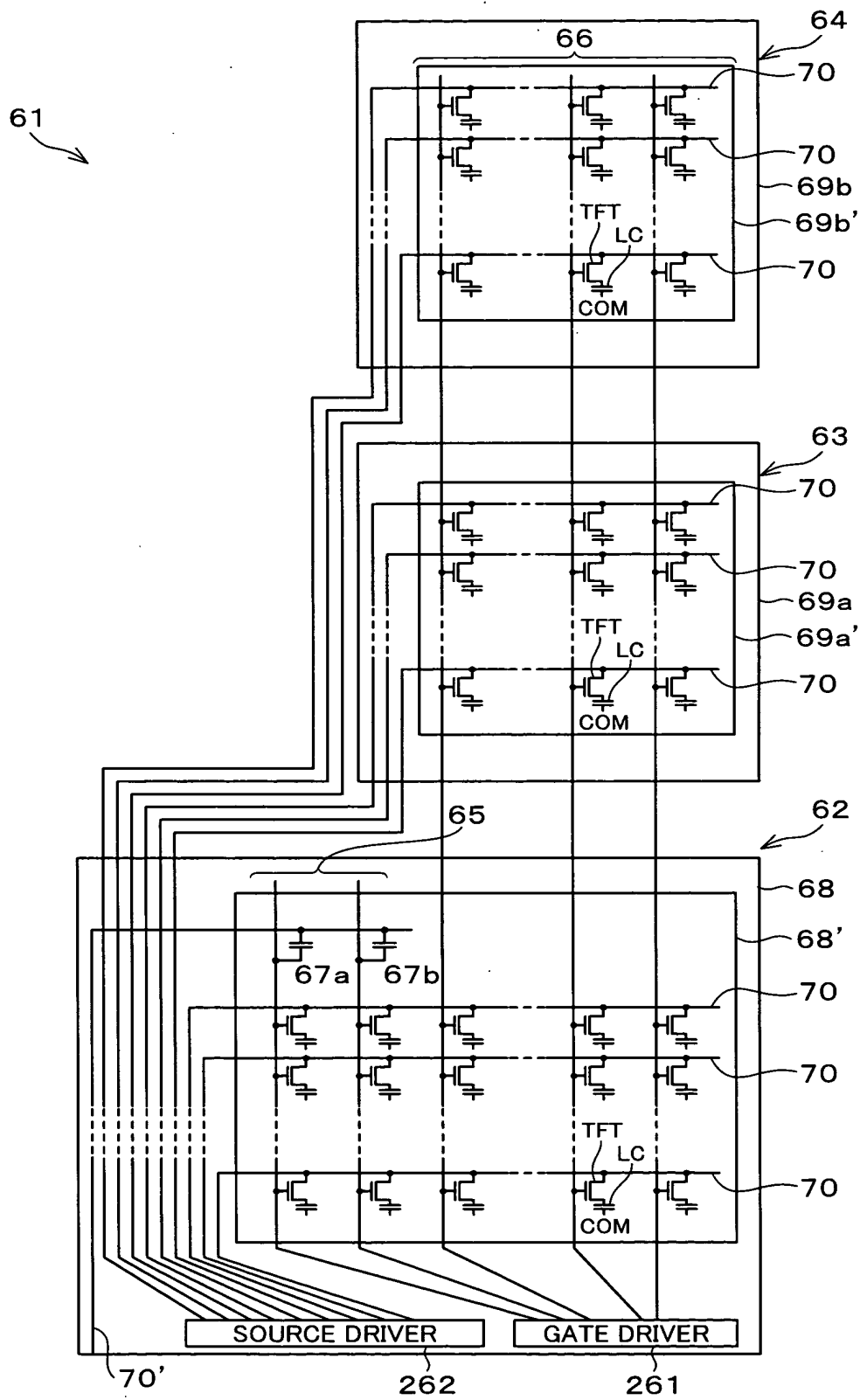


FIG. 15

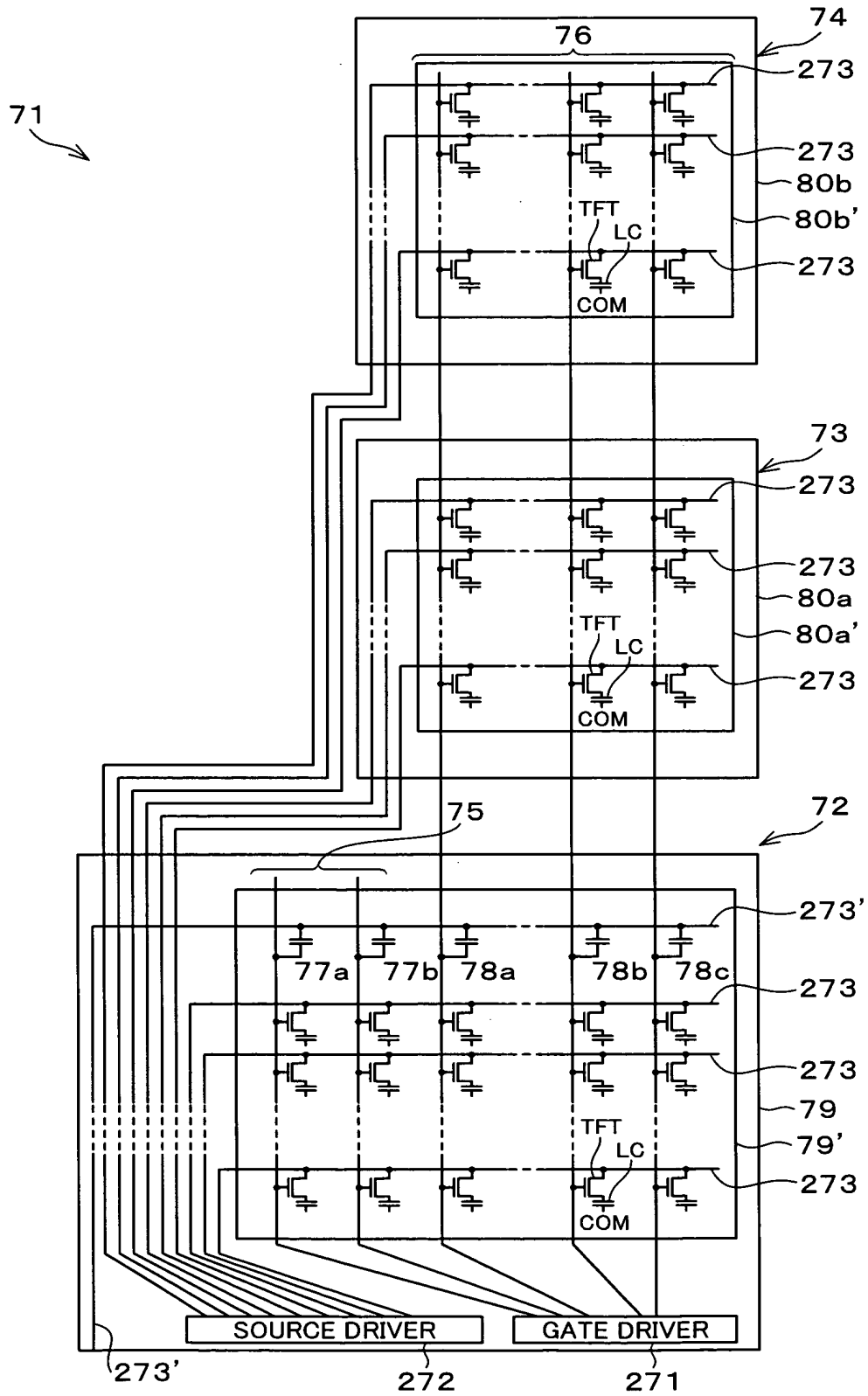


FIG. 16

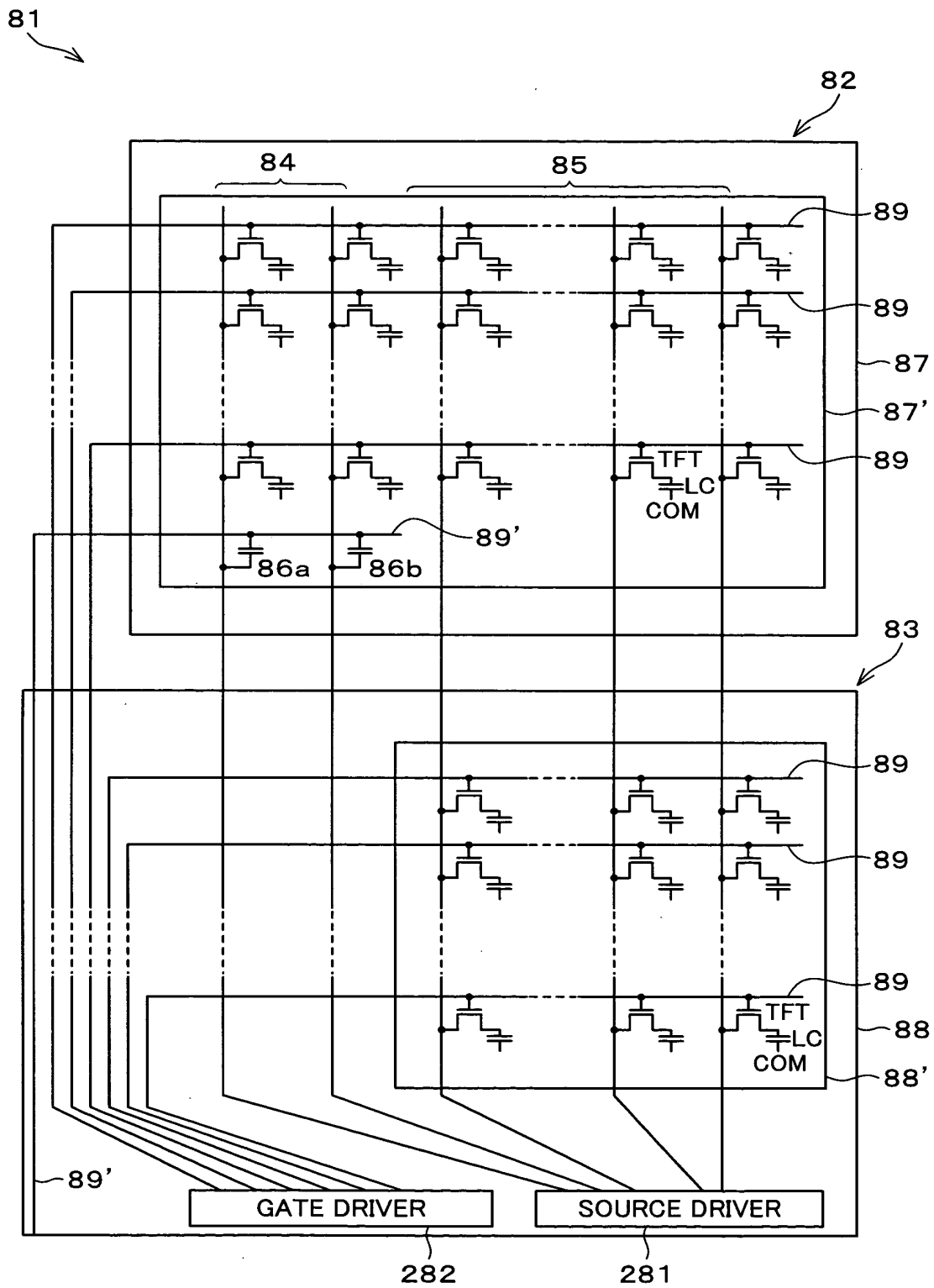
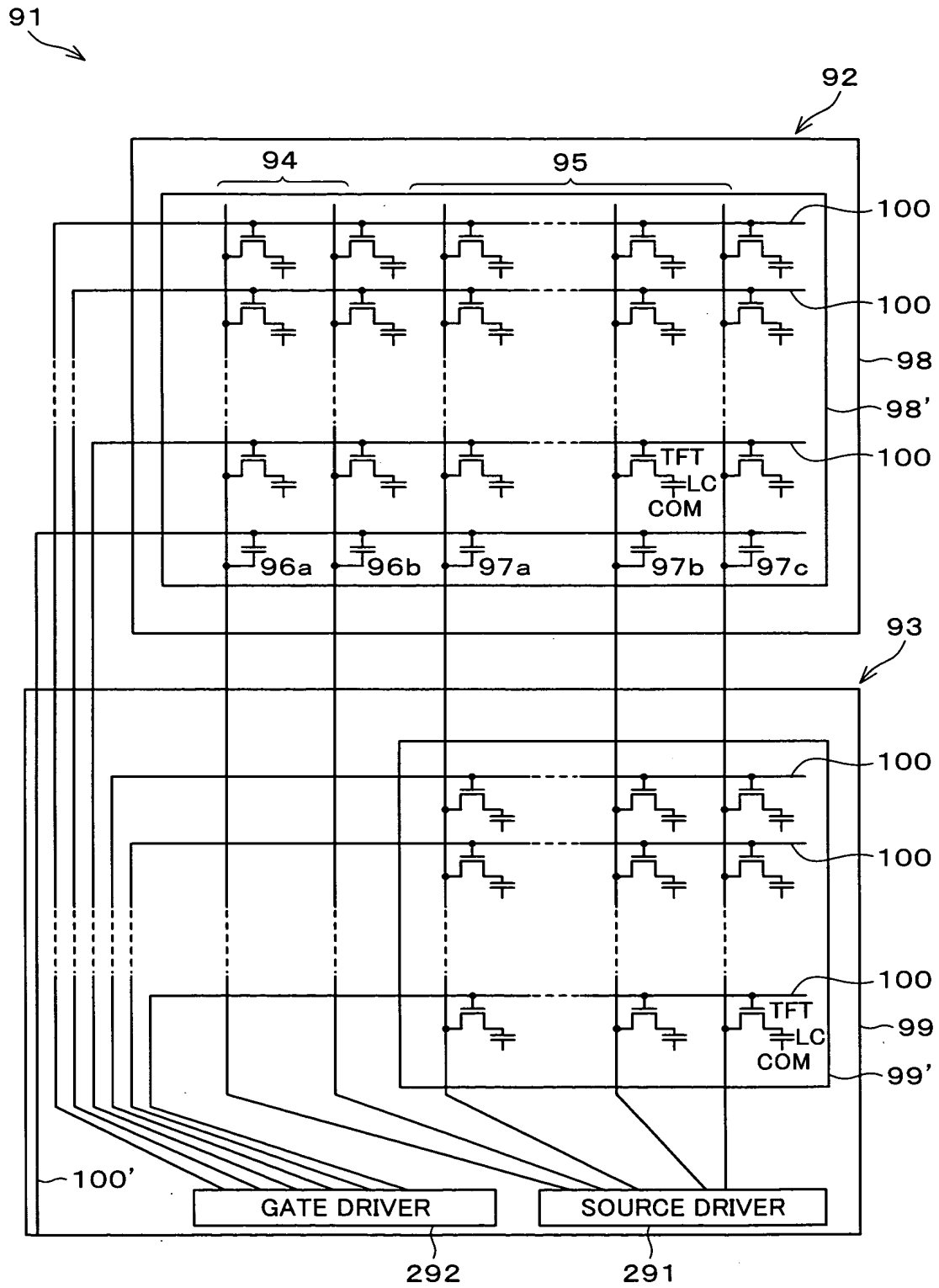




FIG. 17



The diagram illustrates a pixel array 101, which is a grid of pixels 102 and 103. Each pixel contains a TFT (Thin-Film Transistor), LC (Liquid Crystal), and COM (Common) layer. The array is driven by a SOURCE DRIVER 302 and a GATE DRIVER 301. The diagram shows two rows of pixels, 102 and 103, with various circuit components like capacitors and transistors. Labels 104 and 105 indicate specific regions within the array. The diagram also shows a common line 109 and a common line 108. The diagram is labeled with 101, 102, 103, 104, 105, 106a, 106b, 107, 107', 108, 108', 109, 109', 301, and 302.

FIG. 19

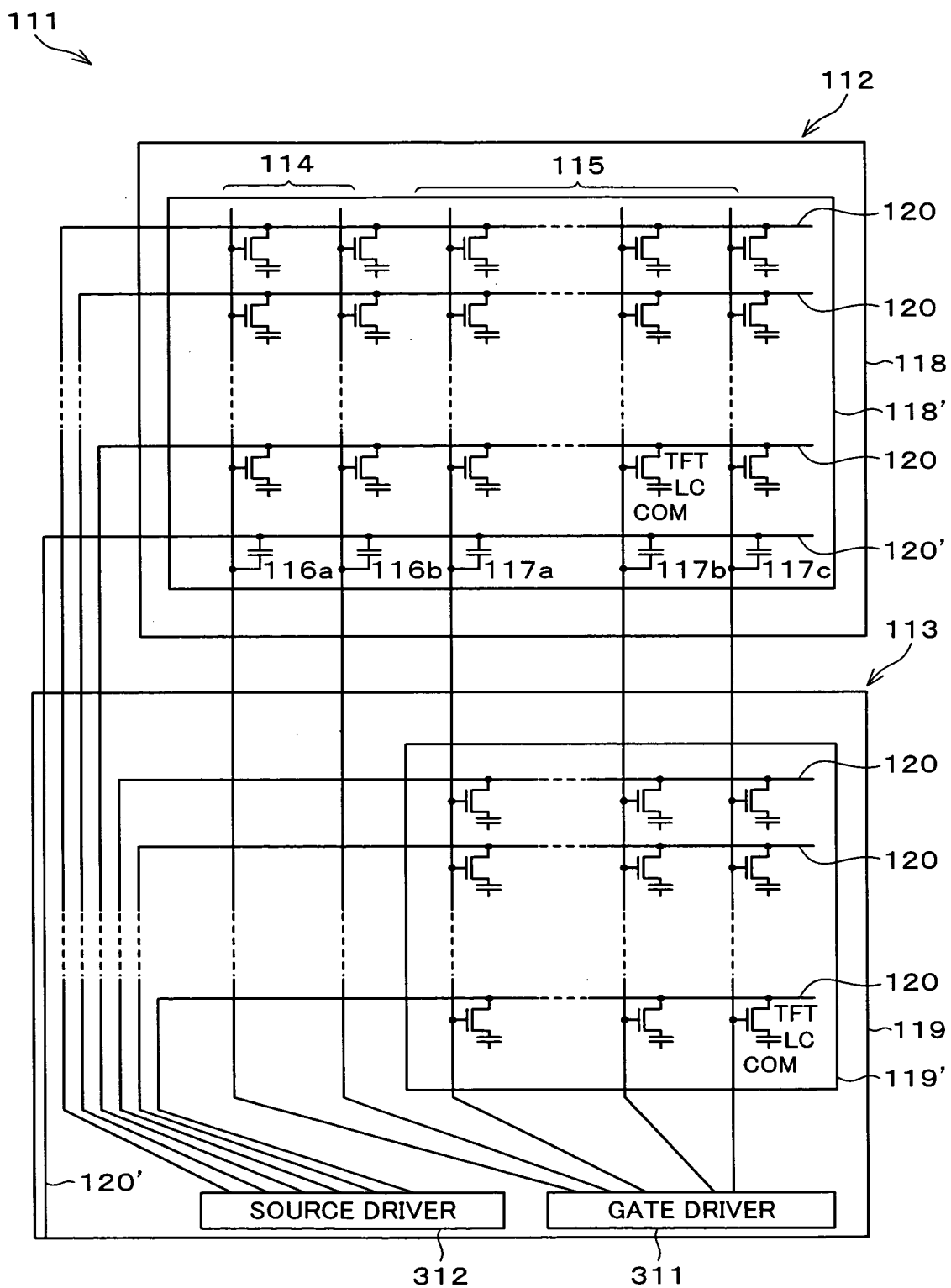


FIG. 20

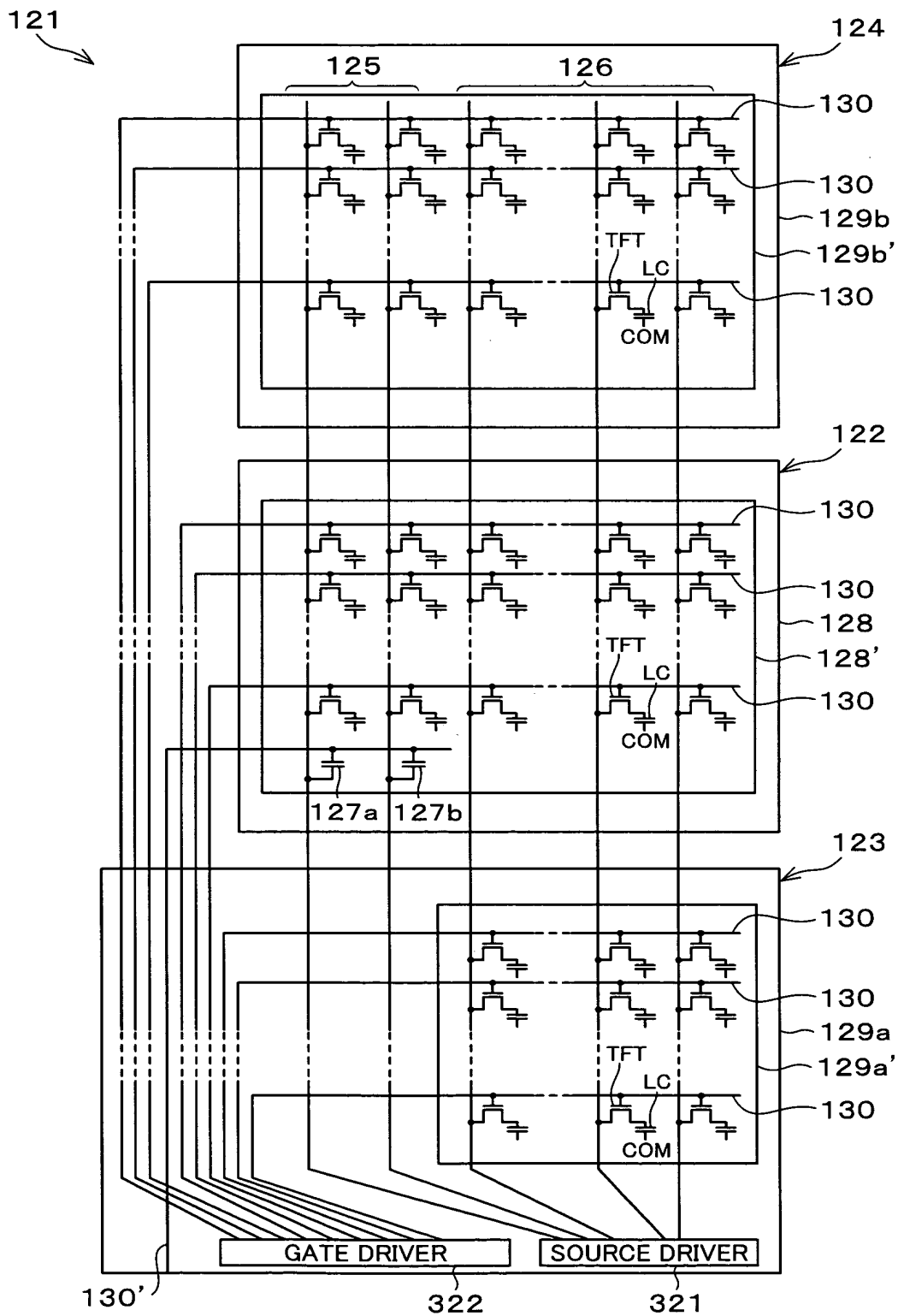


FIG. 21

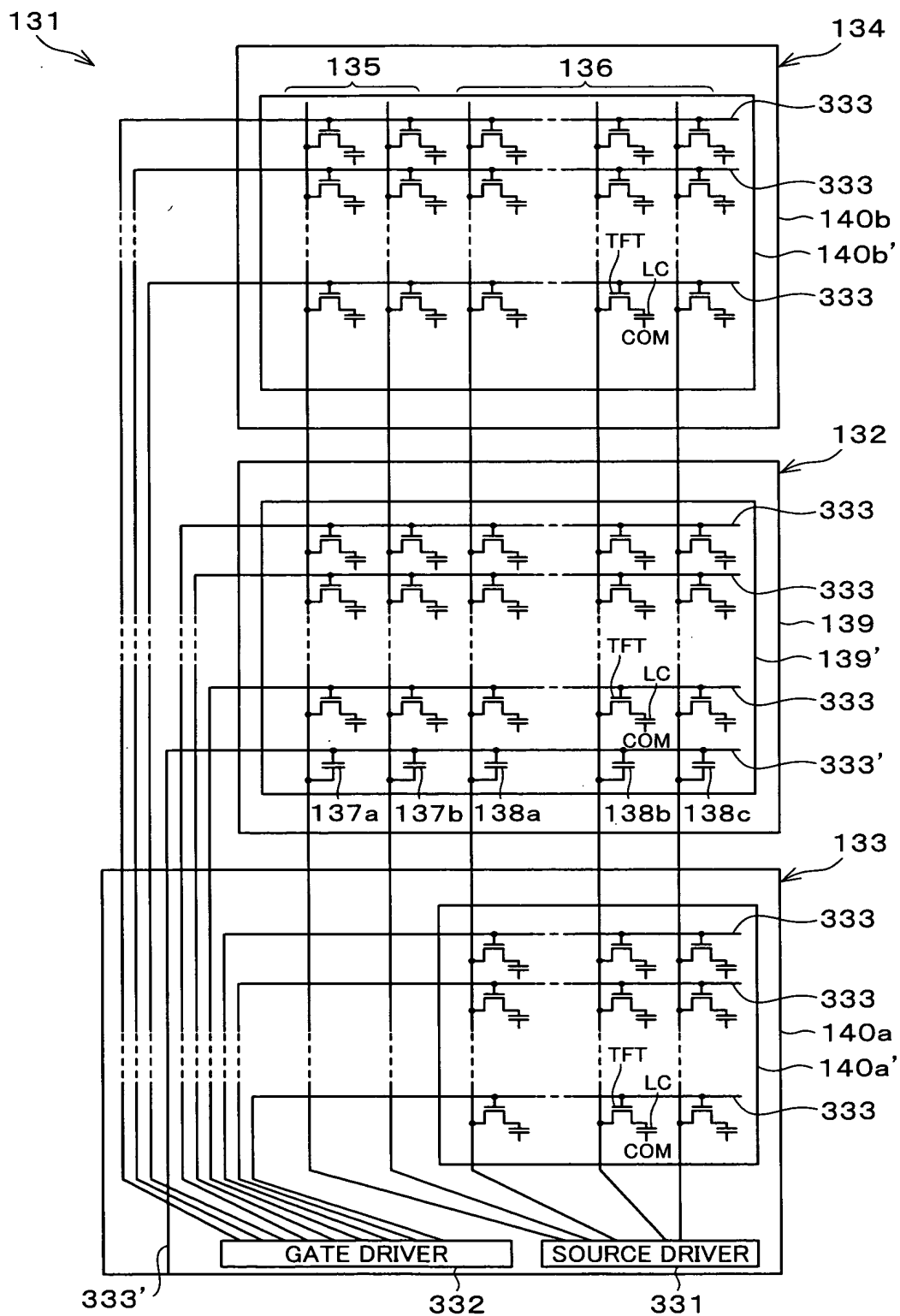


FIG. 22

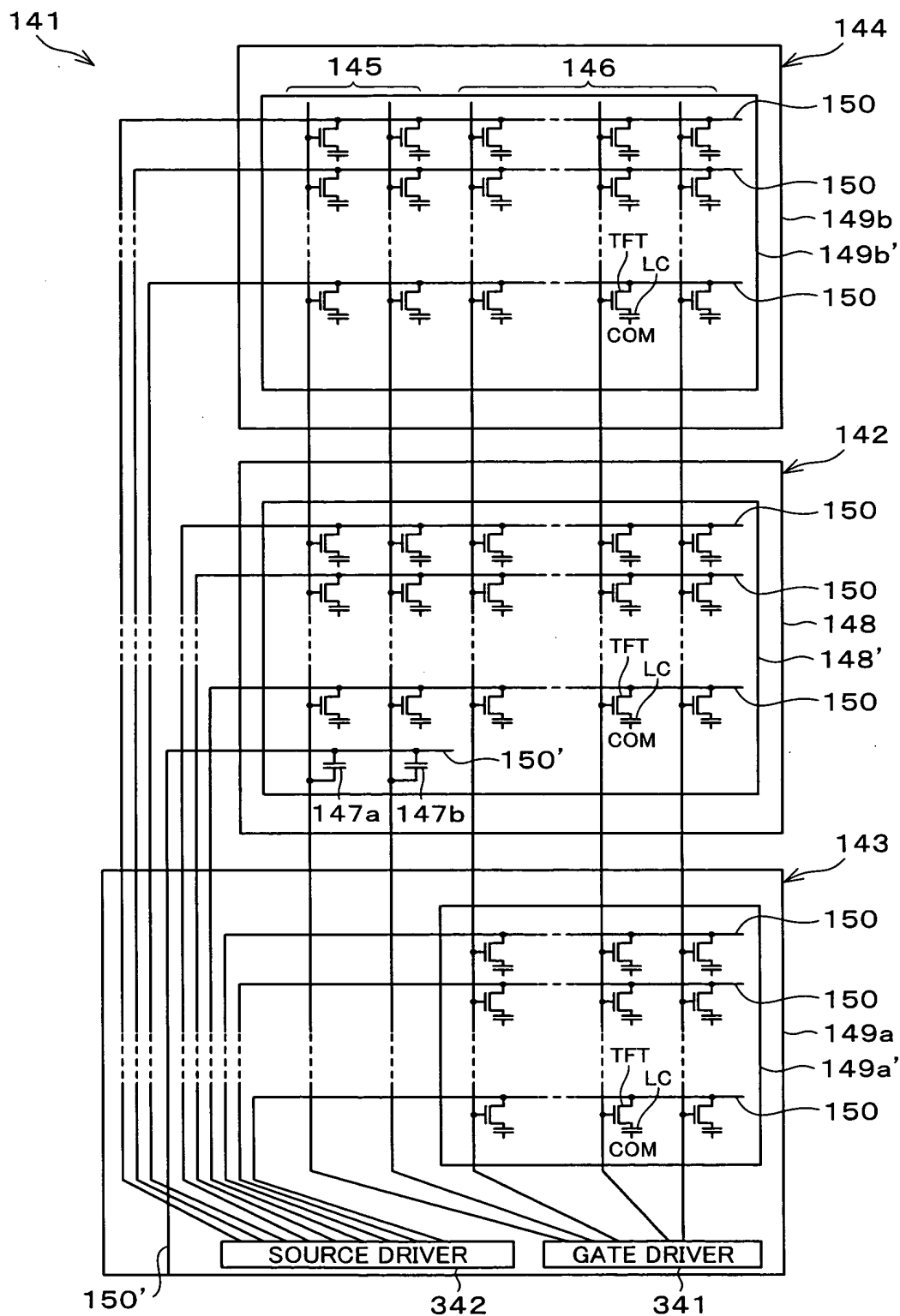


FIG. 23

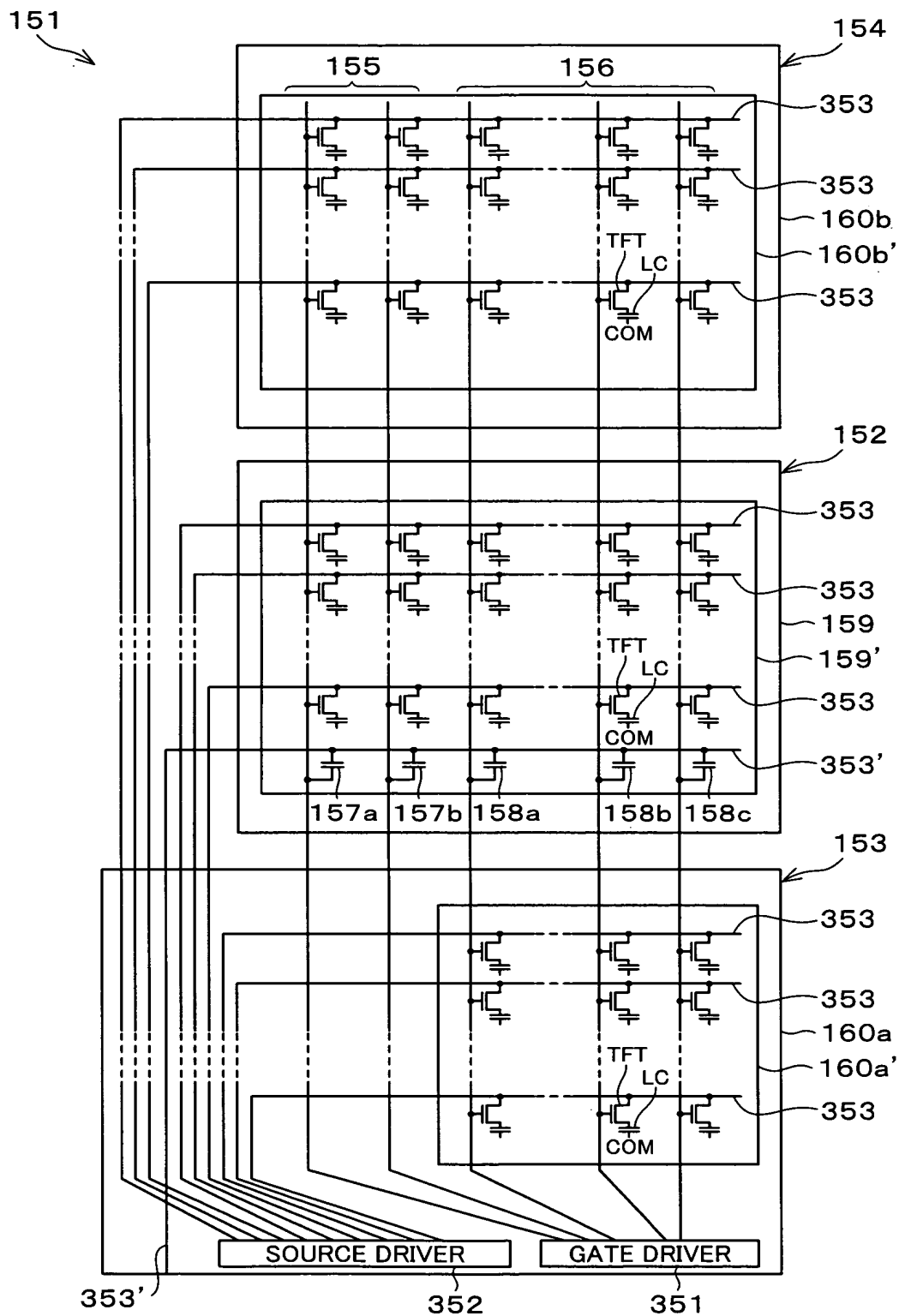


FIG. 24 (a)

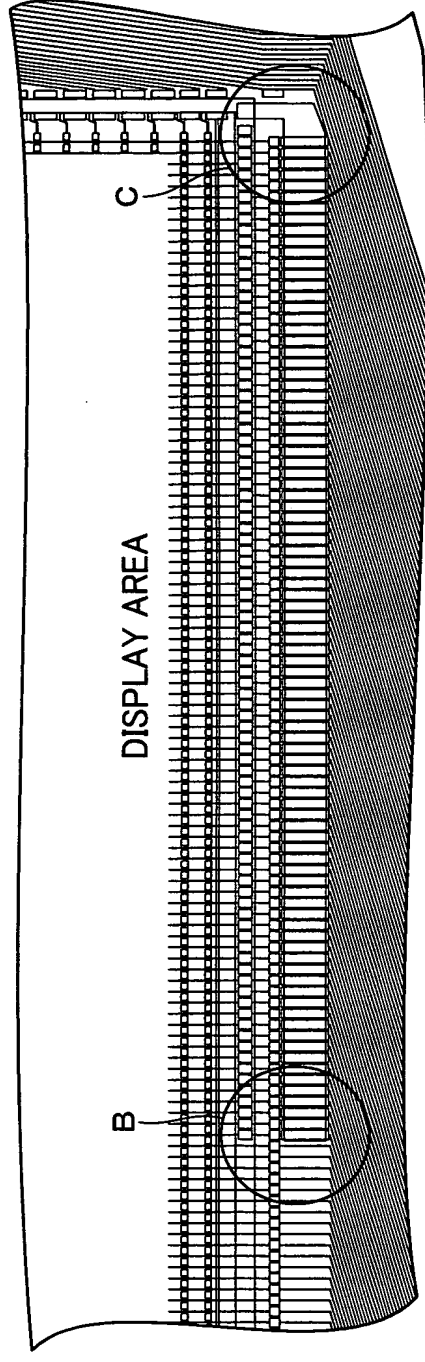


FIG. 24 (b)

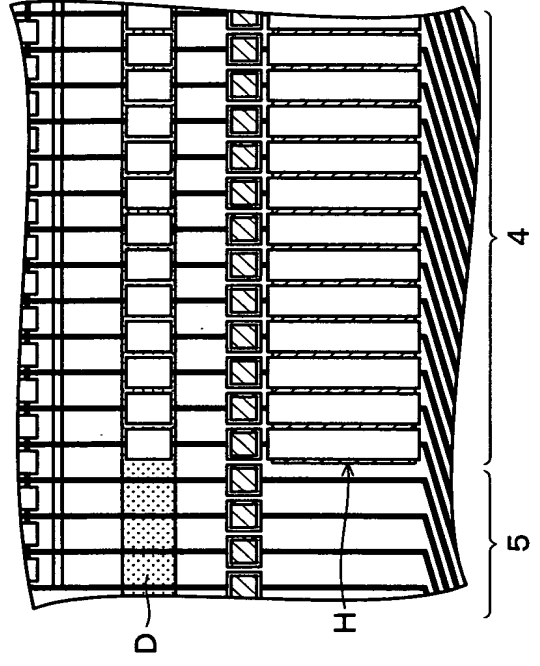


FIG. 24 (c)

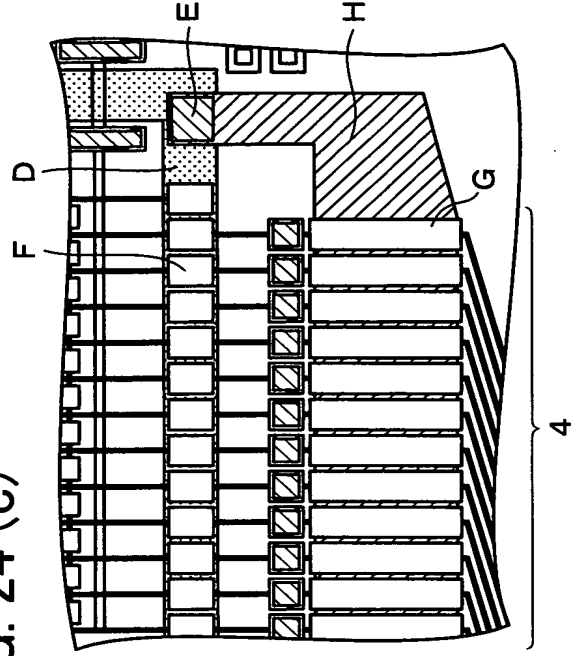




FIG. 25

